

The 7th IEEE International Conference on Artificial Intelligence Circuits and Systems


APRIL 28-30, 2025

BORDEAUX, FRANCE

AICAS 2025: Be a part of a sustAinable future

ABOUT AICAS

Artificial Intelligence is driving a revolution in information technology as well as in many industries. New computing paradigms are required to support the emerging AI algorithms and applications, running on a diversity of platforms, from edge devices to cloud servers and from circuit level to system level. Addressing this challenge and opportunity, the AICAS conference has been established as the premier conference of the IEEE Circuits and Systems Society to promote and facilitate state-of-the-art research, innovation, and development activities at the frontiers of artificial intelligence circuits and systems.

BORDEAUX VENUE

A world-renowned city, Bordeaux is famed for its vineyards, gastronomy, and unique art de vivre. To explore the city and its heritage, you will be spoilt for choice. Whether you wish to wander through Bordeaux's World Heritage area and its beautiful quarters, discover the city's sites and monuments by bike, visit its museums, enjoy a romantic weekend, or travel with your children, you won't be bored! The only risk is that you don't have enough time to do everything: this will only make you want to come back!

PAPER SUBMISSION

Prospective authors are invited to submit anonymously, via the conference website, full length papers in a maximum of 5 pages, of which 4 are for technical content and with an optional 5th page containing only references.

Proposals for special sessions, live demos, and tutorials are welcome.

Submitted papers must describe original work that has not been submitted to other journals or conferences. Authors are expected to present their papers in person at the conference. While the conference proceedings will be submitted to the IEEE Xplore® digital library, the best papers will be recommended to the IEEE Transactions on Circuits and Systems for AI for a Special Issue/Section AICAS2025.

TOPICS OF INTEREST

- In-memory computing
- Circuits and systems for AI
- Architecture for AI computing
- Emerging computing devices and materials for AI
- Circuits and Systems for neural network-based language modeling
- Hardware/software co-design and design automation for AI systems
- Neuromorphic and bioinspired circuits, processors, systems and their applications
- Emerging applications: IoT, healthcare, smart factories, environment, robotics, autonomous vehicles, drones, AR/VR, metaverse
- Computer vision algorithms and architectures
- AI for Future Wireless Communications
- AI for Circuits and Systems
- Compute in Memory

IMPORTANTES DATES

- Special session proposal submission
- Acceptance notification of special session proposal
- Submission deadline (regular and special sessions)
- Tutorial proposal submission
- Live demo proposal submission
- Acceptance notification of tutorial and demo proposals
- Acceptance notification of regular papers
- Acceptance notification of special sessions papers
- Submission of camera-ready papers

August 26, 2024
September 02, 2024
September 23, 2024
September 23, 2024
September 23, 2024
October 22, 2024
December 18, 2024
December 18, 2024
January 10, 2025

www.aicas2025.org

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